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By:

*Malcolm Volk*

Date:

*Sept 25, 2002*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wilhelm Schmid et al.

Applic. No. : 09/740,634

Filed : December 18, 2000

Title : Device for Measurement and Analysis of  
Electrical Signals of an Integrated Circuit  
Component

Examiner : Paresh H. Patel

Group Art Unit : 2829

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A M E N D M E N T

S i r :

Responsive to the non-final Office action dated June 25, 2002,  
kindly amend the above-identified application as follows:

In the Claims:

Claim 1 (amended). An integrated circuit component,  
comprising:

*AI*  
*SLC*  
*BA*  
a plurality of circuit points being not externally accessible,  
providing various electrical signals of the integrated circuit  
component to be measured or analyzed; and